



# Monolithic SiC Semiconductor Switch Development

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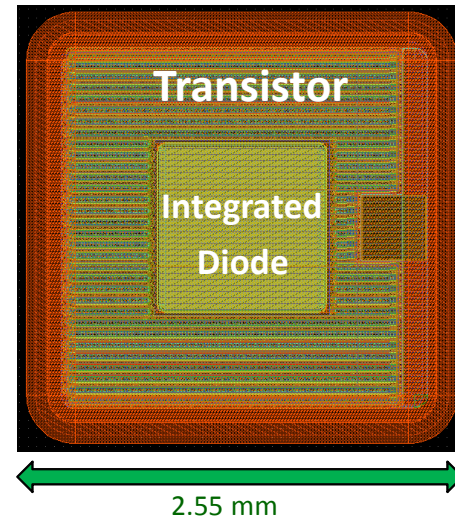
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# Status and Future Efforts

- Future Efforts in Phase II

- Undergoing H-Bridge Circuit Level simulation for Dual-Active Bridge in Energy Storage relevant 400 kW System
- Circuit Efficiencies to be compared between Monolithically Integrated SJT-Diodes with DISCRETE SJT+Diodes



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